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Patent and Trademark Office: U.S. DEPARTMENT OF COMMERCE

**Complete if Known**

<b>Application / Conf. N .</b>	10/001,871 / 3401
<b>Filing Date</b>	November 19, 2001
<b>First Named Inventor</b>	Stephen M. Douglass
<b>Art Unit</b>	2818
<b>Examiner Name</b>	Unknown
<b>Attorney Docket Number</b>	X-919 US

Sheet	1	of	1
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8-4-04

<sup>1</sup> Applicant's unique citation designation number (optional). <sup>2</sup> See Kinds of USPTO Patent Documents at [www.uspto.gov](http://www.uspto.gov) or MPEP 901.04. <sup>3</sup> Enter Office that issued the document, by the two-letter code (WIPO Standard ST.3). <sup>4</sup> For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document. <sup>5</sup> Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST. 16 if possible. <sup>6</sup> Applicant is to place a check mark here if English language Translation is attached.

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Substitute for form 1449A/PTO

## INFORMATION DISCLOSURE STATEMENT BY APPLICANT

(use as many sheets as necessary)

Sheet	1	of	2
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## U.S. PATENT DOCUMENTS

Examiner Initials *	Cite No.¹	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number - Kind Code (if known)			
CSK		US- 5,671,355	09-23-97	Collins	709 250
		US- 5,970,254	10-19-99	Cooke et al.	712 37
		US- 6,020,755	02-01-00	Andrews et al.	326 39
		US- 6,096,091	08-01-00	Hartmann	716 17
		US- 6,279,045 B1	08-21-01	Muthujumaraswathy et al.	710/5
		US- 6,282,627 B1	08-28-01	Wong et al.	712 25
CSIC		US- 6,343,207 B1	01-29-02	Hessel et al.	455 86
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## FOREIGN PATENT DOCUMENTS

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Ken S. Kinn

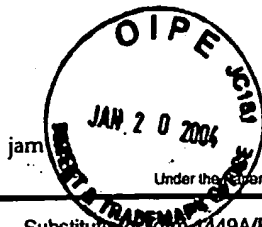
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**INFORMATION DISCLOSURE  
STATEMENT BY APPLICANT**

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Sheet

1

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Examiner Initials *	Cite No. *	Document Number		Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	
		Number - Kind Code (if known)				CL	Sub
KSK		US- 4,758,985		07-19-88	Carter	365	94
		US- 5,142,625		08-25-92	Nakai	710	260
		US- 4,855,669		08-08-89	Mahoney	324	73.1
		US- RE34,363		08-31-93	Freeman	326	38
		US- 5,072,418		12-10-91	Boutaud et al.	708	207
		US- 5,274,570		12-28-93	Izumi et al.	716	1
		US- 5,550,782		08-27-96	Cliff et al.	365	230.03
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		US- 5,311,114		05-10-94	Sambamurthy et al.	370	296
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		US- 09/991,412	11-16-01 02-16-96		Kimura et al. Heron		
		US- 5,457,410		10-10-95	Ting	326	41
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		US- 5,581,745		12-03-96	Muraoka	703	502
		US- 5,742,179		04-21-98	Sasaki	326	39
		US- 5,600,845		02-04-97	Gilson	712	39
		US- 5,574,930		11-12-96	Halverson Jr., et al.	712	34

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Examiner Initials *	Cite No. *	Foreign Patent Document		Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T *
		Country Code - Number *	Kind Code * (if known)				
KSK		EP 0315275 A2		10-05-89	LSI Logic	- / -	
KSK		WO 93 25968 A1		12-23-93	Furtek	- / -	
KSK		EP 0 905 906 A2		03-31-99	Lucent	- / -	
KSK		EP 1 235 351 A1		08-28-02	Matsushita Electric	- / -	

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Signature

Ken S. Kim

Date  
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8-4-04

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<b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b>  (use as many sheets as necessary)		Application / C nf. N .	10/001,871 / 3401		
		Filing Date	November 19, 2000		
		First Named Inventor	Stephen M. Douglass		
		Art Unit	2818		
		Examiner Name	Unknown		
Sheet	2	of	7	Attorney Docket Number	X-919 US

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OTHER - NON PATENT LITERATURE DOCUMENTS			
Examiner Initials *	Cite No <sup>2</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T <sup>2</sup>
CSK		SAYFE KIAEI et al., "VLSI DESIGN OF DYNAMICALLY RECONFIGURABLE ARRAY PROCESSOR-DRAP," IEEE, February 1989, pp. 2484-2488, V3.6, IEEE, 3 Park Avenue, 17th Floor, New York, NY 10016-5997	
CSK		VASON P. SRINI, "FIELD PROGRAMMABLE GATE ARRAY (FPGA) IMPLEMENTATION OF DIGITAL SYSTEMS: AN ALTERNATIVE TO ASIC," IEEE, May 1991, pp. 309-314, IEEE, 3 Park Avenue, 17th Floor, New York, NY 10016-5997	
CSK		G. MAKI et al., "A RECONFIGURABLE DATA PATH PROCESSOR," IEEE, August 1991, pp. 18-4.1 to 18-4.4, IEEE, 3 Park Avenue, 17th Floor, New York, NY 10016-5997	
CSK		IACOB DAVIDSON, "FPGA IMPLEMENTATION OF RECONFIGURABLE MICROPROCESSOR," IEEE, March 1993, pp. 3.2.1 - 3.2.4, IEEE, 3 Park Avenue, 17th Floor, New York, NY 10016-5997	
CSK		CHRISTIAN ISELI et al., "BEYOND SUPERSCALER USING FPGA's," IEEE, April 1993, pp. 486-490, IEEE, 3 Park Avenue, 17th Floor, New York, NY 10016-5997	
CSK		P.C. FRENCH et al., "A SELF-RECONFIGURING PROCESSOR," IEEE, July 1993, pp. 50-59, IEEE, 3 Park Avenue, 17th Floor, New York, NY 10016-5997	
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CSK		MICHAEL J. WIRTHLIN et al., "THE NANO PROCESSOR: A LOW RESOURCE RECONFIGURABLE PROCESSOR," IEEE, February 1994, pp. 23-30, IEEE, 3 Park Avenue, 17th Floor, New York, NY 10016-5997	
CSK		WILLIAM S. CARTER, "THE FUTURE OF PROGRAMMABLE LOGIC and ITS IMPACT ON DIGITAL SYSTEM DESIGN," April 1994, IEEE, pp. 10-16, IEEE, 3 Park Avenue, 17th Floor, New York, NY 10016-5997	
CSK		ANDRE' DEHON, "DPGA-COUPLED MICROPROCESSORS: COMMODITY ICs FOR THE EARLY 21ST CENTURY," IEEE, February 1994, pp. 31 - 39, IEEE, 3 Park Avenue, 17th Floor, New York, NY 10016-5997	
CSK		OSAMA T. ALBAHARNA, "AREA & TIME LIMITATIONS OF FPGA-BASED VIRTUAL HARDWARE," IEEE, April 1994, pp. 184 - 189, IEEE, 3 Park Avenue, 17th Floor, New York, NY 10016-5997	

Examiner Signature	<i>Ken S. Kim</i>	Date Considered	<i>2-4-04</i>
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Substitute for form 1449A/PTO

## INFORMATION DISCLOSURE STATEMENT BY APPLICANT

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Sheet	3	of	7
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**Complete if Known**

Application / C / No.	10/001,871 / 3401
Filing Date	November 19, 2001
First Named Inventor	Stephen M. Douglass
Art Unit	2818
Examiner Name	Unknown
Attorney Docket Number	X-919 US

## U.S. PATENT DOCUMENTS

Examiner Initials *	Cite No.¹	Document Number	Publication Date MM-DD-YYYY	Name of Patentes or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number - Kind Code (if known)			
KSK		US- 5,473,267	12-05-95	Stansfield	326 41
		US- 5,742,180	04-21-98	DeHon et al.	326 40
		US- 5,737,631	04-07-98	Trimberger	712 37
		US- 5,748,979	05-05-98	Trimberger	712 37
		US- 09/991,410	11-16-01	Herron et al.	
		US- 6,026,481	02-15-00	New et al.	712 43
		US- 5,705,938	01-06-98	Kean	326 39
		US- 5,500,943	03-19-96	Ho et al.	712 218
		US- 6,467,009	10-15-02	Winegarden et al.	710 305
		US- 5,809,517	09-15-98	Shimura	711 115
		US- 5,543,640	08-06-96	Sutherland et al.	257 202
		US- 5,574,942	11-12-96	Colwell et al.	712 23
		US- 6,604,228	08-05-03	Patel et al.	716 8
		US- 5,933,023	08-03-99	Young	326 40
		US- 2003/0062922	04-03-03	Douglass et al.	326 39
		US- 6,389,558	05-14-02	Herrmann et al.	714 39
		US- 5,732,250	03-24-98	Bates et al.	713 600
		US- 5,889,788	03-30-99	Pressly et al.	714 726
	US- 5,874,834	02-23-99	New	326 39	
103K		US- 5,835,405	11-10-98	Tsui et al.	365 182

## FOREIGN PATENT DOCUMENTS

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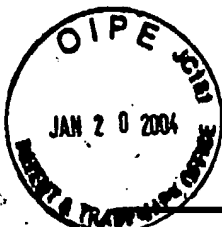
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		Filing Date	November 19, 2001			
		First Named Inventor	Stephen M. Douglass			
		Art Unit	2818			
		Examiner Name	Unknown			
Sheet	4	of	7	Attorney Docket Number	X-919 US	JAN 22 2004

OTHER - NON PATENT LITERATURE DOCUMENTS			Technology Center 2100
Examiner Initials *	Cite No.	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T <sup>2</sup>
CSK		XILINX, INC., "THE PROGRAMMABLE LOGIC DATA BOOK," 1994, Revised 1995, Xilinx, Inc., 2100 Logic Drive, San Jose, CA. 95124.	
CSK		XILINX, INC., "THE PROGRAMMABLE LOGIC DATA BOOK," 1994, Revised 1995, pp 2-109 to 2-117, Xilinx, Inc., 2100 Logic Drive, San Jose, CA. 95124.	
CSK		XILINX, INC., "THE PROGRAMMABLE LOGIC DATA BOOK," 1994, Revised 1995, pp 2-9 to 2-18; 2-187 to 2-199, Xilinx, Inc., 2100 Logic Drive, San Jose, CA. 95124.	
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CSK		WILLIAM B. ANDREW et al., "A FIELD PROGRAMMABLE SYSTEM CHIP WHICH COMBINES FPGA & ASIC CIRCUITRY," IEEE, May 16, 1999, pp. 183-186, IEEE, 3 Park Avenue, 17th Floor, New York, NY 10016-5997	
CSK		XILINX, INC., "THE PROGRAMMABLE LOGIC DATA BOOK," 2000, Ch. 3 pp 3-1 TO 3-117, Xilinx, Inc., 2100 Logic Drive, San Jose, CA 95124	

Examiner Signature	Ken S. Kim	Date Considered	F-4-04
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Sheet	6	of	7
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Applicati n / Conf. N .	10/001,871	/	3401
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<b>Filing Dat</b>	<b>November 19, 2001</b>
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<b>First Named Inventor</b>	Stephen M. Douglass
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Art Unit	2818
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Examiner Name	Unknown
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Attorney Docket Number	X-919 US
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### OTHER - NON PATENT LITERATURE DOCUMENTS

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KSK		US- 6,519,753	02-11-03	Ang	216 56 16
		US- 6,434,735	08-13-02	Watkins	716 16
		US- 6,588,006	07-01-03	Watkins	716 13
		US- 6,539,508	03-25-03	Patrie et al.	714 726
		US- 6,587,995	07-01-03	Duboc et al.	716 4
		US- 6,242,945	06-05-01	New	326 39
		US- 6,460,172	10-01-02	Insenser Farre et al.	716 17
		US- 6,353,331	03-05-02	Shimanek	326 39
		US- 6,507,942	01-14-03	Calderone et al.	716 16
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		Country Code *	Number * Kind Code * (if known)				

Examiner Signature	<u>Ken S. Kim</u>	Date Considered	<u>8-4-04</u>
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